

राष्ट्रीय प्रौद्योगिकी संस्थान पुदुच्चेरी

नेहरू नगर, कारैक्काल– 609 605

NATIONAL INSTITUTE OF TECHNOLOGY PUDUCHERRY Nehru Nagar, Karaikal – 609 605

Advertisement for the Position of Research Associate (RA)

Applications are invited from eligible candidates for the position of **Research Associate (RA)** under the Special Manpower Development Programme for Chip to Systems Development **(SMDP-C2SD)** project carried out by Department of ECE.

Funding Agency of the Project: DeitY/MCIT, New Delhi

Name of the post: Research Associate

Number of Vacancy: 01.

Responsibilities:

- 1. Analysis and verification of collected data
- 2. Maintenance and development of lab equipment
- 3. Carrying R&D work
- 4. Guiding UG students on their project work in speech processing, VLSI and related works.
- 5. Assisting students during the lab classes on speech processing, VLSI and DSP
- 6. Undergoing training in advanced topics on signal processing and VLSI
- 7. Preparation of project reports to the project monitoring and project funding group
- 8. Developing the skills on advanced signal/speech processing and VLSI
- 9. Any other administration duties assigned by CI or Co-CI

Salary and Tenure:

The initial salary may be fixed from Rs.30, 000 (consolidated) for the RA depending upon the previous experience. The post is purely temporary in nature. The appointment may initially be made for a period of 1 year and the tenure may be extended for an additional 1 year depending upon the performance.

Educational Qualifications:

The minimum qualification is I Class M.E. / M.Tech and B.E/ B.Tech in Signal Processing / VLSI System with sound working knowledge on VLSI CAD Tools and relevant areas.

The completely filled in application form, summary sheet and all necessary enclosures be sent in a sealed cover Super scribed with "Application for the post of Research Associate" under SMDP-C2SD Project and the application should be sent to: The Registrar, SMDP-C2SD project, PKCE Campus, Nehru Nagar, NIT Puducherry, Karaikal – 609605.

The filled application should reach the mentioned address by speed post latest by **July 22, 2016**.

Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained. The interview date will be informed by e-mail or telephone & the list will be displayed on institute website. Original documents of age proof/certificates/degree and other testimonials along with photocopies must be produced at the time of interview.

Note: No TA/DA will be paid for attending the interview.